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**United States Patent [19]****Uyesugi et al.****[11] Patent Number: 5,949,777****[45] Date of Patent: Sep. 7, 1999****[54] WIRELESS COMMUNICATION PROCESSOR SYSTEM**

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**[56] References Cited****U.S. PATENT DOCUMENTS**

4,056,779 11/1977 Toler ..... 455/11.1

4,528,656	7/1985	Morais .....	370/69.1
4,639,914	1/1987	Winters .....	455/54.1
5,200,951	4/1993	Grau et al. ....	370/261
5,241,542	8/1993	Natarajan et al. ....	370/95.3
5,271,007	12/1993	Kurahashi et al. ....	370/85.1
5,282,204	1/1994	Shpancer et al. ....	370/95.1
5,349,694	9/1994	Kawano et al. ....	455/11.1
5,371,734	12/1994	Fischer .....	370/95.1
5,551,066	8/1996	Stillman et al. ....	455/69

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**[57] ABSTRACT**

A wireless communication processor system includes a wireless data/control commands transmission/reception unit, buffer memory for storing wirelessly received data and control commands, buffer memory for storing data and control commands for wireless transmission, buffer memory for receiving and storing data and control commands from host units such as computers and printers, buffer memory for storing data and control commands to be transmitted from the processor to a host unit, and a system for establishing a link with other wireless communication processors for transmitting data and control commands to, or for receiving data and control commands from, the other processors.

**32 Claims, 11 Drawing Sheets**